

**UNIVERSITY COLLEGE OF ENGINEERING
OSMANIA UNIVERSITY, HYDERABAD**

M.E. (ECE, Mech. & CSE) I - Semesters (Main) Examination

December 2018/January 2019

MVSR ENGINEERING COLLEGE

REVISED EXAMINATION TIME TABLE

TIME : 2.00 PM TO 5.00 PM

**FOR BACKLOG STUDENTS
OF AFFILIATED COLLEGES
PRIOR TO 2017 BATCH**

**Examination Centre:
Concerned Dept.
UCE (A), O.U.**

DATE & DAY	E. C. E. (EMBEDDED SYSTEMS & VLSI DESIGN)	MECHANICAL (CAD / CAM)	C. S. E.
	I-Semester	I-Semester	I-Semester
21-12-2018 FRIDAY	Analog IC Design	=====	Advanced Algorithms
24-12-2018 MONDAY	Digital Signal Processors	=====	Artificial Intelligence
27-12-2018 THURSDAY	=====	=====	Mobile Computing
29-12-2018 SATURDAY	=====	=====	Advanced Operating Systems
31-12-2018 MONDAY	VLSI Design and Technology	Finite Element Techniques	=====
02-01-2019 WEDNESDAY	=====	=====	=====
04-01-2019 FRIDAY	=====	=====	=====



DIRECTOR OF EVALUATION
EXAMINATION CELL