

**UNIVERSITY COLLEGE OF ENGINEERING
OSMANIA UNIVERSITY, HYDERABAD
M.E. (E.C.E.) I-Semester Regular & PTPG and III-Semester PTPG (Main) Examination
December 2018/January 2019**

**Examination Centre:
Dept. of ECE
UCE (A), O.U.**

REVISED EXAMINATION TIME TABLE

Time : 2.00 PM TO 5.00 PM

Date and Day	Digital Systems			Systems & Signal Processing	Microwave & Radar Engineering		Embedded Systems and VLSI Design
	Regular I-Semester	PTPG I-Semester	PTPG III-Semester	Regular I-Semester	Regular I-Semester	PTPG I-Semester	Regular I-Semester
21-12-2018 FRIDAY	Field Programmable Gate Arrays	=====	Field Programmable Gate Arrays	Digital Control	=====	=====	Embedded Systems Design
24-12-2018 MONDAY	Digital Design and PLDs	Digital Design and PLDs	Adhoc Wireless & Sensor Networks	Modern Digital Signal Processing	Microwave Circuits	Microwave Circuits	Principles of VLSI System Design
27-12-2018 THURSDAY	Scripting Languages for VLSI Design Automation	=====	GNSS Signals & Receiver Technology	=====	Microwave Measurement	=====	=====
29-12-2018 SATURDAY	Microcontroller Architecture	Microcontroller Architecture	=====	Real Time Signal Processing	Microwave Antennas	Microwave Antennas	Satellite Navigation Systems
31-12-2018 MONDAY	VLSI Design and Technology	VLSI Design and Technology	Advanced Computer Organization	Speech Signal Processing	Advanced Electromagnetic Engineering	Advanced Electromagnetic Engineering	CPLD & FPGA Architectures and Applications
02-01-2019 WEDNESDAY	Analog and Mixed Signal IC Design	=====	=====	Optimization Techniques	Satellite & Microwave Communication	=====	Analog and Mixed Signal IC Design
04-01-2019 FRIDAY	=====	=====	Wireless Communication and Networks	Wireless Communication and Networks	Wireless Communication and Networks	=====	Microwave Integrated Circuits
07-01-2019 MONDAY	Engineering Research Methodology						

**Note: This Time-Table is also applicable for Pre-Ph.D. students
(Not applicable for 2017- 18 Batch.)**


DIRECTOR OF EVALUATION
EXAMINATION CELL